The present application was filed on July 15, 2003 with claims 1-19. Claim 2 was previously canceled, and claims 1 and 3-19 were pending prior to the present amendment. Claims 1, 18 and 19 are the pending independent claims.

In the Decision on Appeal dated June 15, 2009 (hereinafter "the Decision"), the Board affirmed the Examiner's rejections of claims 1, 3, 4 and 6-19 under 35 U.S.C. §103(a) as being unpatentable over Boggio et al., "NetworkDesigner - Artifex - OptSim: A Suite of Integrated Software Tools for Synthesis and Analysis of High Speed Networks," Optical Networks Magazine, Sept/Oct 2001, pages 27-41 (hereinafter "Boggio") in view of Sun et al., "Simulation Studies of Multiplexing and Demultiplexing Performance in ATM Switch Fabrics," Performance Engineering in Telecommunications Network Teletraffic Symposium, 14-16 Apr. 1993, pages 21/1 – 21/5 (hereinafter "Sun") and of claim 5 under 35 U.S.C. §103(a) as being unpatentable over Boggio and Sun in view of Ishida et al., "A 10-GHz 8-b Multiplexer/Demultiplexer Chip Set for the SONET STS-192 System," IEEE Journal of Solid-State Circuits, Vol. 26, No. 12, Dec. 1991, pages 1936-1943 (hereinafter "Ishida").

In this response, Applicants have amended claims 1, 18 and 19 without prejudice in order to more clearly and distinctly set forth the subject matter which Applicants regard as the invention. More specifically, claim 1 has been amended to specify that the at least one switch fabric comprises a plurality of integrated circuits of a designated chip set, that the one or more configurable parameters of the switch fabric comprise one or more configurable parameters of each of the integrated circuits and one or more configurable parameters of a base device specified for the designated chip set. Each integrated circuit of the designated chip set corresponds to a specified ingress device, a specified cross-connect device or a specified egress device and the one or more configurable parameters of a given integrated circuit are determined by the correspondence of the given integrated circuit to the specified device.

Support for these amendments may be found in the specification at, for example, page 6, lines 1-10, 15-17 and 25-28; and page 7, line 22, to page 8, line 18. In an illustrative embodiment, a switch fabric configuration may be generated using a particular chip set, namely, the PI40 chip set. The PI40 chip set includes the devices PI40Xi (an ingress device), PI40C (a cross-connect device), and PI40Xe (an egress device), and a given PI40 switch fabric configuration comprises one or more of these three devices. As shown in FIG. 2, a given chip

set device 210 in the set of chip set devices 208 may be selected by a user, and the configurable parameters <u>for the selected device</u> are then shown. For example, in FIG. 2, <u>configurable parameters of a PI40Xi device</u> are shown. FIG. 3 allows input of configurable parameters for a PI40Base device specified for the aforementioned PI40 chip set. An automatic configuration generation processes the values of the configuration parameters entered via the portions of the user interface shown in FIGS. 2 and 3 and automatically generates a corresponding switch fabric configuration suitable for use in a system simulation in the development tool 100. Applicants note that this amendment incorporates certain limitations of dependent claims 5-7.

Applicants respectfully submit that the cited combination of Boggio, Sun and Ishida fails to teach or suggest the limitations of amended claim 1. The combined teachings of these references fail to teach or suggest any arrangement wherein an interface permits user control of one or more configurable parameters of the switch fabric and automatically generating a simulation configuration for the switch fabric based on current values of the configurable parameters of the switch fabric, wherein the one or more configurable parameters of the switch fabric comprise one or more configurable parameters of each of the integrated circuits and one or more configurable parameters of a base device specified for the designated chip set, with each integrated circuit of the designated chip set corresponding to a specified ingress device, a specified cross-connect device or a specified egress device, and the one or more configurable parameters of a given integrated circuit being determined by the correspondence of the given integrated circuit to the specified device.

Corresponding amendments have been made to independent claims 18 and 19, which are therefore believed to be similarly patentable over the cited references.

Dependent claims 3, 4, 6 and 8-17 are believed allowable for at least the reasons identified above with regard to claim 1.

Applicants have also added new dependent claims 20-23, which are believed allowable for at least the reasons identified above with regard to claim 1. Moreover, each of newly-added dependent claims 20-23 is believed to be separately patentable over the cited references.

Dependent claim 20 specifies that the interface permits user selection of <u>each</u> of a centralized configuration, a stackable configuration and a distributed configuration for the switch fabric. Support for this amendment may be found in the specification at, for example, page 8, lines 24-27. Applicants respectfully submit that the cited references fail to teach or suggest any

arrangement which permits selection of <u>each</u> of a centralized configuration, a stackable configuration and a distributed configuration for the switch fabric. Applicants note that, in affirming the rejection of claim 10 in the Decision at page 9, last paragraph, the Board found that Sun appears to only disclose a centralized configuration.
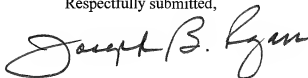
New dependent claim 21 specifies that the one or more configurable parameters of the switch fabric comprise a clock speed of at least a given one of the integrated circuits and a clock speed of the base device specified for the designated chip set. Support for this amendment may be found in FIGS. 2 and 3, as well as the specification at, for example, page 8, lines 12-14.

New dependent claim 22 specifies that the switching capacity of the switch fabric determines a number of integrated circuits included in the switch fabric. Support for this amendment may be found in the specification at, for example, page 7, lines 11-16.

New dependent claim 23 specifies that each integrated circuit has at least one block function associated therewith. Support for this amendment may be found in the specification at, for example, page 4, lines 13-17, with reference to FIG. 1.

In view of the foregoing, amended claims 1, 3, 4, 6 and 8-23 are believed to be in condition for allowance.

Respectfully submitted,

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